



PCISIB PCI Sync Interface Board

Overview

The PCI Sync Interface Board (PCISIB) provides a Real-Time clock for PCI-based computers via an external or an internally generated interrupt.



Features

- 32 bit/33 MHz half-length Universal PCI board compatible with 32 or 64 bit, 33 or 66 MHz PCI slots.
- External interrupt is software programmable to trigger on rising or falling edge.
- External interrupt can be immediate or it can be delayed with a programmable delay control. The programmable delay range is 100ns to 9 minutes in 100ns increments.
- External interrupt signals are conditioned on an application specific daughter board that is attached to the PCISIB. The daughter board converts signals such as differential TTL or video sync to standard TTL levels thus eliminating the need for external signal conditioning.
- External interrupt connects through a DB25 connector.
- Internal interrupt provides an internal Real-Time clock interrupt with a period of up to 9 minutes, in 100ns increments.
- Two rear mounted LEDs provide visual indication of interrupt activity.
- Two user programmable rear mounted LEDs are provided.
- Modular Advanced Common Computing Environment (MACCE) support

Highly configurable

All logic on the PCISIB is performed inside a Field Programmable Gate Array (FPGA), allowing for custom configuration of input, outputs, and functions. The daughter board interface allows for multiple inputs and outputs.

Software Drivers

Linux software drivers come standard with the PCISIB.