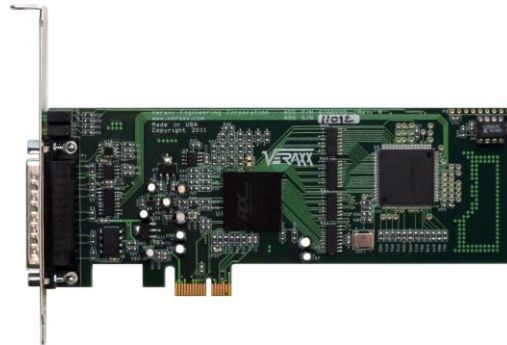




PCIESIB PCIe Sync Interface Board

Overview

The PCIe Sync Interface Board (PCIESIB) provides a Real-Time clock for PCI Express based computers via an external or an internally generated interrupt.



Features

- Half-height half-length PCI Express x1 board.
- External interrupt is software programmable to trigger on rising or falling edge.
- External interrupt can be immediate or it can be delayed with a programmable delay control. The programmable delay range is 100ns to 9 minutes in 100ns increments.
- External interrupts connect through a DB25 connector.
 - Differential TTL (4)
 - Video sync (2)
- Internal interrupt provides an internal Real-Time clock interrupt with a period of up to 9 minutes, in 100ns increments.
- Two rear mounted LEDs provide visual indication of interrupt activity.
- Two user programmable rear mounted LEDs are provided.

Highly configurable

- All logic on the PCIESIB is performed inside a Field Programmable Gate Array (FPGA), allowing for custom configuration of input, outputs, and functions.

Software Drivers

- 32 and 64 bit Linux software drivers come standard with the PCIESIB.

Veraxx Engineering Corporation

14130 Sullyfield Circle, Suite B, Chantilly, VA 20151 • 703-880-9000 • Fax: 703-880-9005

www.veraxx.com